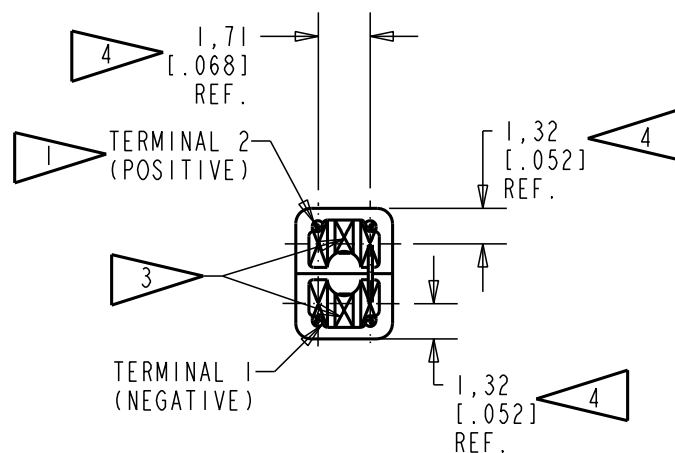
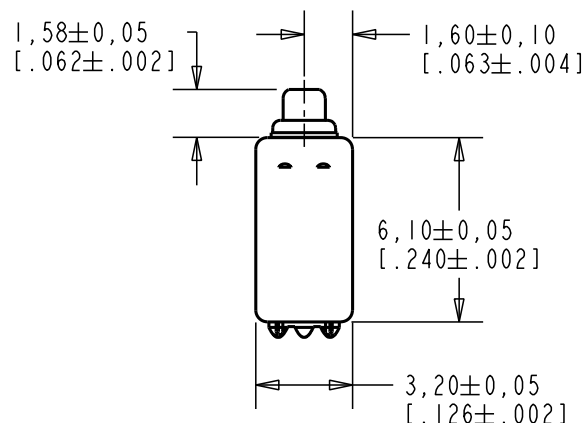


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SHT 1.1

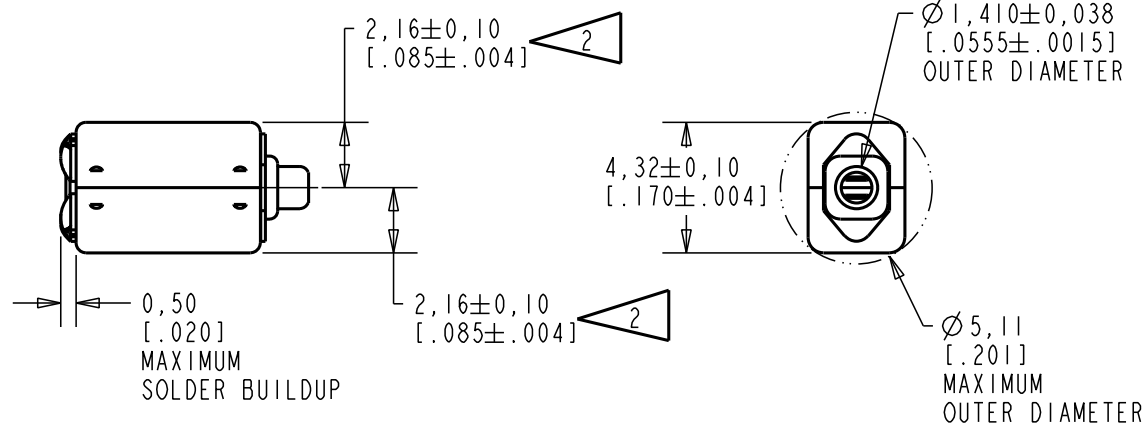
NOTES:

- 1 A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO TERMINAL 1, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET.
- 2 LOCATED FROM TWO SURFACE FOR CUSTOMER CONVENIENCE. ONLY APPLICABLE FROM ONE SURFACE, NOT TO BE USED TOGETHER.
- 3 TERMINAL ELECTRICALLY CONNECTED TO CASE.
- 4 DIMENSION TO APPROXIMATE CENTER OF TERMINAL PAD.



SCALE 2:1
NOMINAL WEIGHT
0.25 GRAMS

DIMENSIONS IN MILLIMETERS [INCHES]



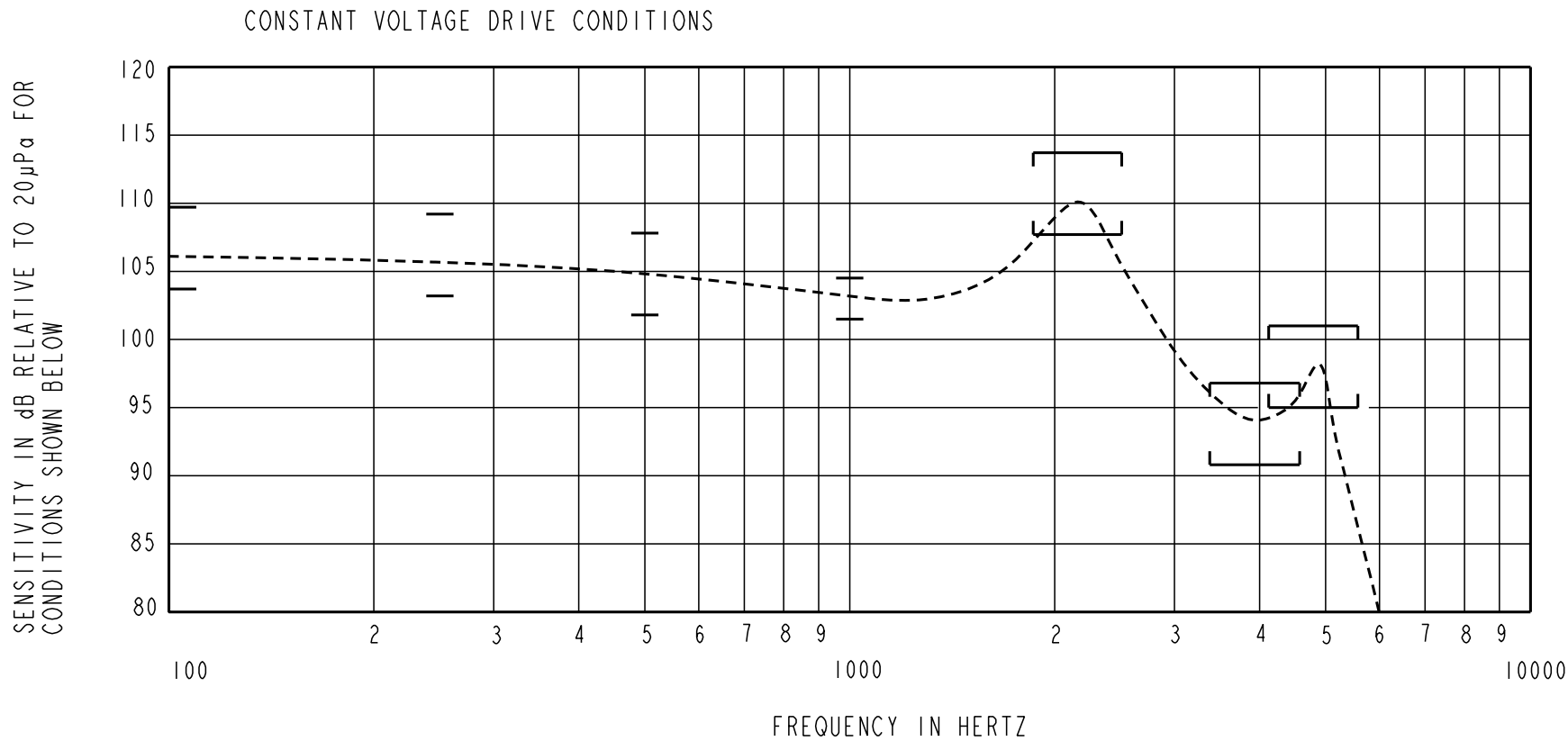
Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
B	PI0000365	1-25-16	Active	B
A	CI0116621	3-18-15		
SCALE: 4:1			DR. BY LW	DATE 3-18-15
DO NOT SCALE DRAWING			CK. BY GJP	DATE 3-20-15
TITLE: RECEIVER		GR-31653-000		APP. BY GJP
OUTLINE DRAWING		SHT 1.1		DATE 3-20-15

KNOWLES CORPORATION

THIS IS A PAIR OF RECEIVERS WITH VERY LOW VIBRATION IN ALL DIRECTIONS, AND A QUADRAPOLE MAGNETIC RADIATION PATTERN.

NO DAMPING

GR-31653-000
SHEET 2.1



ACOUSTICAL

SENSITIVITY
DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS DESCRIBED IN TABLE 4. NOMINAL SENSITIVITY AT 1kHz IS dB RELATIVE TO 20µPa. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT 1kHz.

LIMIT TYPE	FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
REL	100	+0.7	+3.7	+6.7
REL	250	+0.2	+3.2	+6.2
REL	500	-1.2	+1.8	+4.8
REF	1000	-1.5	103.0	+1.5
PEAK	1860 - 2510	+4.7	+7.7	+10.7
VALLEY	3380 - 4580	-12.2	-9.2	-6.2
PEAK	4125 - 5580	-8.0	-5.0	-2.0

TABLE 1

TOTAL HARMONIC DISTORTION
DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (Vrms)	DC BIAS (V)	LIMIT (%)
1/3 PEAK	0.188	0	3
1/2 PEAK	0.188	0	3
1/3 PEAK	0.530	0	8
1/2 PEAK	0.530	0	8

TABLE 2

MAXIMUM OUTPUT LEVEL

POWER (mW)	500 Hz SPL (dB)	REQUIRED VOLTAGE (Vrms)	Peak SPL (dB)	REQUIRED VOLTAGE (Vrms)
10	118.3	1.257	129.4	2.099

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.188 Vrms, 0 mA DC BIAS
SOURCE IMPEDANCE	<1 Ohm
TUBING	10 mm [.394"] LONG X 1 mm [.039"] I.D. ("ITE")
COUPLER CAVITY	2 CM ³ , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 60318-5)
MAXIMUM DRIVE VOLTAGE	2.605 Vrms

TABLE 3

ELECTRICAL

DC RESISTANCE @ 20°C	114.0 Ohms ± 10%
IMPEDANCE @ 500 Hz	158.0 Ohms ± 15%
IMPEDANCE @ 1 kHz	253.0 Ohms ± 15%
INDUCTANCE @ 500 Hz	36.1 mH TYPICAL
CAPACITANCE @ 10 MHz	2.8 pF TYPICAL

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

MECHANICAL

PORT LOCATION: 12S

SOLDER TYPE: SAC305

TEMPERATURE
OPERATING: SENSITIVITY WILL NOT VARY MORE THAN +1/-3 dB AT 500 Hz FROM -17°C TO 63°C

STORAGE: -40°C TO 63°C

SHOCK RESISTANCE: 90% SURVIVAL RATE WITH THD @ 1/3 PEAK FREQUENCY LESS THAN 10%, THD @ 1/2 PEAK FREQUENCY LESS THAN 20% AND LESS THAN 3dB CHANGE IN SENSITIVITY AT 1kHz WHEN SUBJECTED TO 14,100 G.

KNOWLES CORPORATION

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
B	PI0000365	1-25-16	Active	B
A	CI0116621	3-18-15		
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION			DR. BY LW	DATE 3-18-15
TITLE: RECEIVER			CK. BY GJP	DATE 3-20-15
PERFORMANCE SPECIFICATION			APP. BY GJP	DATE 3-20-15
			GR-31653-000 SHT 2.1	